

ACKNOWLEDGEMENT RECEIPT

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**Title of
Invention**

**METHOD FOR MANUFACTURING WAFER LEVEL CHIP SCALE
PACKAGE STRUCTURE**

Submission Type : Utility Patent Filing

Application Number:

10/711536



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First Named Applicant: Min-Chih Hsuan

Attorney Docket Number: 13301-US-PA

Timestamp: 2004-09-24 02:27:38 EDT

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File Listing:

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us-request	13301-usrequ.xml	3437	2004-09-24
us-request	us-request.dtd	19064	2004-09-24
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us-assignment	13301-usasn.xml	2489	2004-09-24
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us-assignment	us-assignment.dtd	11123	2004-09-24
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us-assignment	13301assignment02.tif	16118	2004-09-24
us-fee-sheet	13301-usfees.xml	1640	2004-09-24
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application-body	soamsb.ent	3988	2004-09-24
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application-body	mathml2.dtd	54588	2004-09-24
application-body	mathml2-qname-1.mod	13225	2004-09-24
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application-body	mmlextra.ent	7901	2004-09-24
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application-body	wipo.ent	4956	2004-09-24
application-body-pdf-wrap	13301usf-pdf-wrap.xml	737	2004-09-24
abstract-pdf	13301usf-abst.pdf	14798	2004-09-24
claims-pdf	13301usf-clms.pdf	22384	2004-09-24
description-pdf	13301usf-desc.pdf	50488	2004-09-24
drawings-pdf	13301usf-draw.pdf	79773	2004-09-24
us-declaration	13301declaration01.tif	43158	2004-09-24
us-declaration	13301declaration02.tif	36992	2004-09-24
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